Electronic Acknowledgement Receipt					
EFS ID:	4222968				
Application Number:	10072145				
International Application Number:					
Confirmation Number:	1448				
Title of Invention:	Semiconductor device and method of providing regions of low substrate capacitance				
First Named Inventor/Applicant Name:	Guy E. Averett				
Correspondence Address:	ON Semiconductor Patent Administration Dept - MD A700 P.O. Box 62890 - Phoenix AZ 85082-2890 US 6022445603 -				
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
Request for Continued Examination (RCE)	ONS00317RCE_110308.pdf	595249	. no	3	
		d32314ff31211b0305b33734483a398d4a5 bc707			
Warnings:					
Information:					
2 Fee Worksheet (PTO-06)	fee-info.pdf	29912	no	2	
		dd7b4a8c6708e4bb679b29f1c18504d4bea 09c39			
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